

Abstracts

Improving the CAD of SRD Frequency Multipliers

J. Zhang and A.V. Raisanen. "Improving the CAD of SRD Frequency Multipliers." 1996 MTT-S International Microwave Symposium Digest 96.3 (1996 Vol. III [MWSYM]): 1767-1769.

A method for improving the efficiency of CAD of SRD frequency multipliers is proposed. By abating the nonlinearity of the model of the diode to an appropriate extent, the simulation and optimization of SRD frequency multipliers can be carried out faster and easier. Simulation results are compared with experimental results.

[Return to main document.](#)